

_	Application No.	Applicant(s)
	10/018,606	OKAMOTO ET AL.
	Examiner	Art Unit
	Jennifer A. Poker	2832

	SEAR	CHED	
Class	Subclass	Date	Examiner
336	200	8/16/2004	JP
336	232	8/16/2004	JP
336	223	8/16/2004	JP
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INI	ERFERENC	E SEARCH	ED
Class	Subclass	Date	Examiner
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	DATE	EXMR
East Text Search: inductor and substrate and (insulat\$3 dielectric) and (imped\$4 capacit\$3 resist\$3) and diode FET	8/16/2004	JP